

DATA ARRAY HAVING REDUNDANCY MESSAGING BETWEEN ARRAY CONTROLLERS OVER THE HOST BUS

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FIG. 1

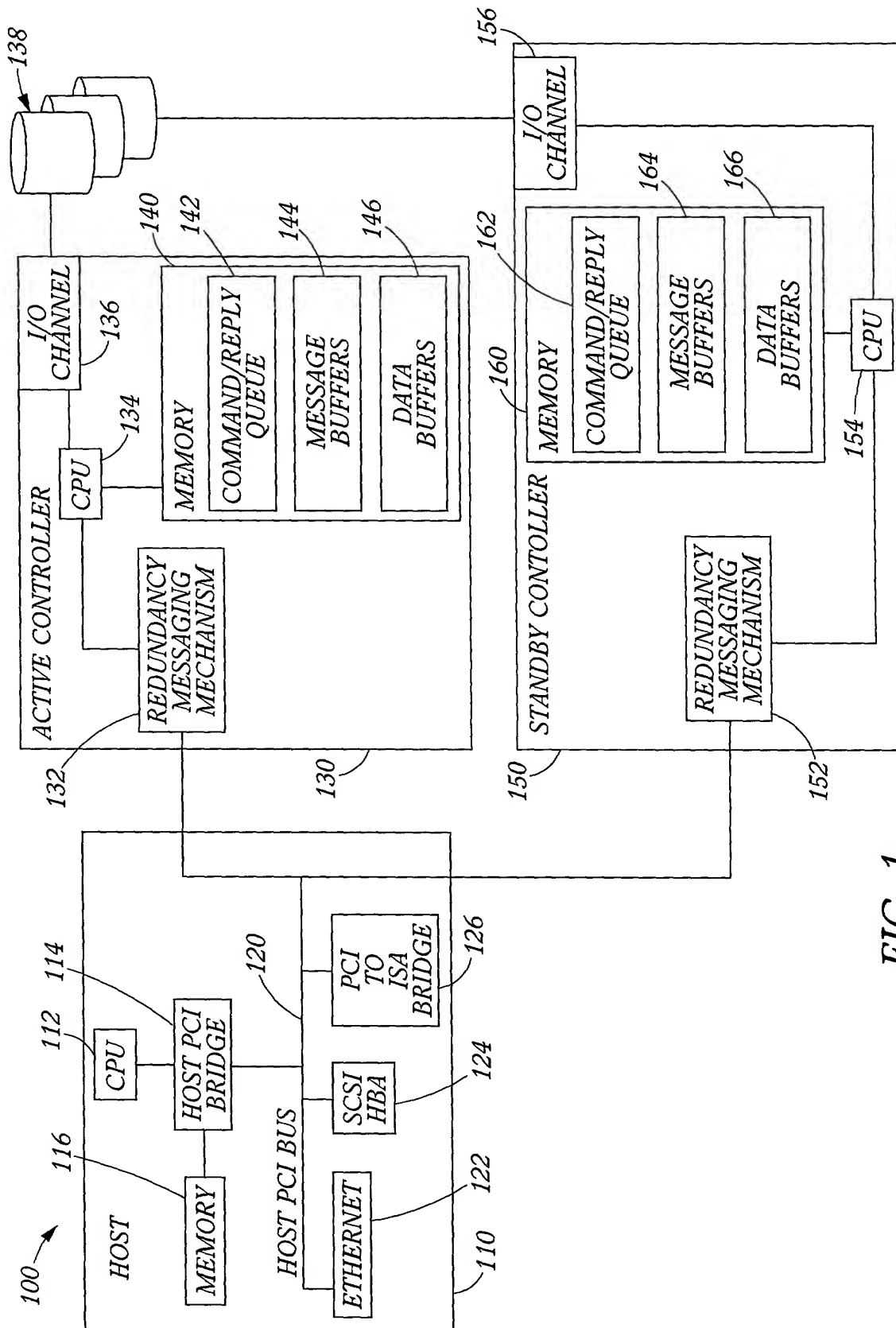


FIG. 1

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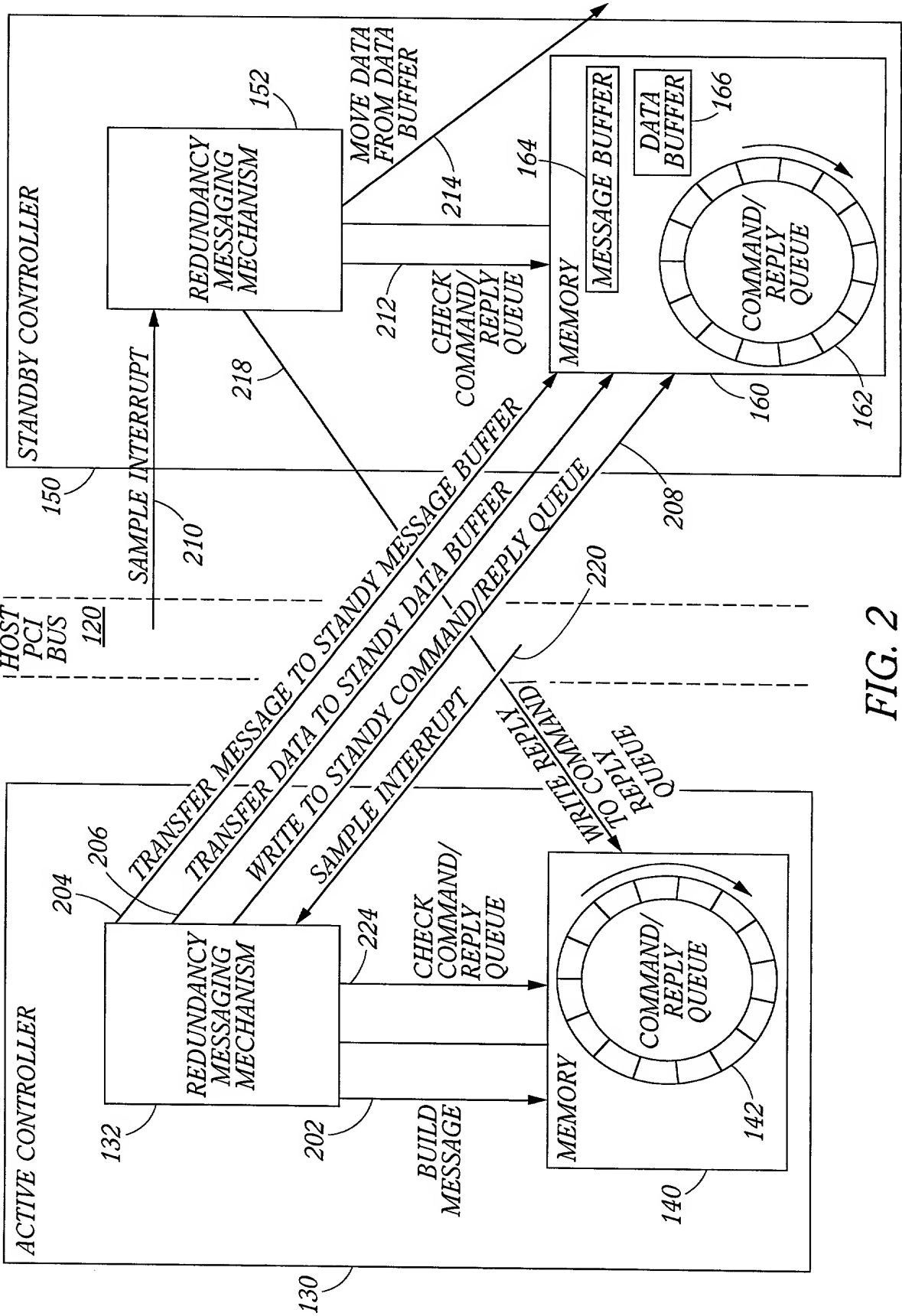


FIG. 2

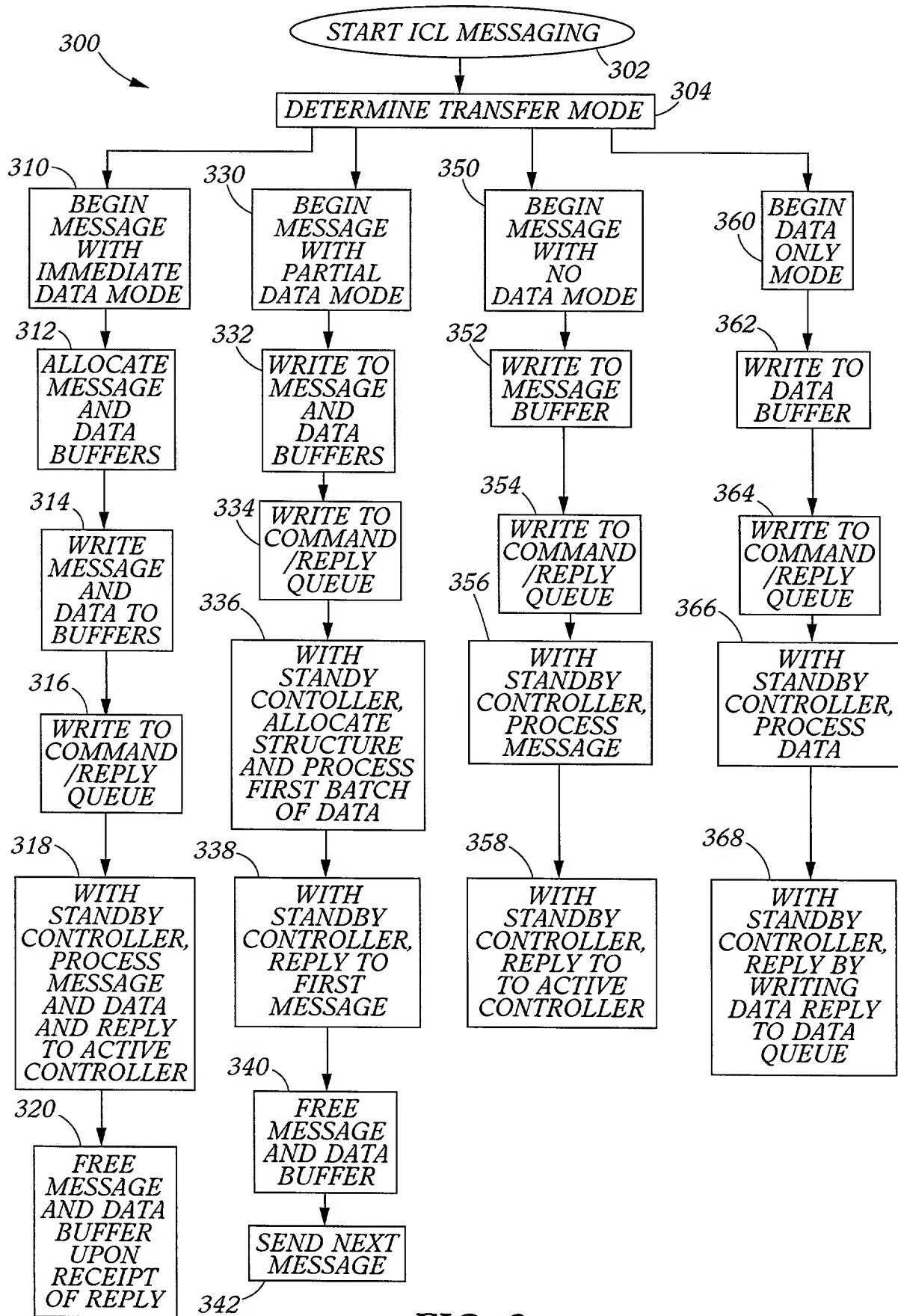


FIG. 3